U.S. ASSIGNMENT

IN CONSIDERATION of the sum of One Dollar (\$1.00), and of other good and valuable consideration paid to the undersigned inventor(s) (hereinafter ASSIGNOR) by

FUJITSU LIMITED, 1015, Kamikodanaka, Nakahara-ku, Kawasaki-shi, Kanagawa, 211

	(hereinafter ASSIGNEE), the receipt of which is hereby ack transfers to ASSIGNEE the entire and exclusive right, title		
tle of ention)	METHOD FOR PRODUCING SEMICONDUCTOR INTEGRA IN SUCH METHOD	TED CIRCUITS AND APPARA	ATUS USED
•	for which application for Letters Patent of the United State	s was	<u></u>
Check	② executed on even date herewith,		
e Box)	☐ filed on, Serial No	,	O N
•	and all Letters Patent of the United States to be obtained therefor on said application or any continuation, division, renewal, substitute reissue or reexamination thereof for the full term or terms for which the same may granted.		
	The ASSIGNOR agree(s) to execute all papers necessary in connection with this application and any continuing,—divisional, reissue or reexamination applications thereof and also to execute separate assignments in connection with such applications as the ASSIGNEE may deem necessary or expedient.		
	with such approactions as the 7500101122 may decin necessi	ily of expedient.	
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